

IN THE SPECIFICATION

Amendments to the Specification:

On page 3, paragraph [0006], please replace the first sentence beginning “Synthesis tool 53 analyzes the logic function and presents the IC designer with an implementation of that function using various types of standard cells that meet the specification”, with the following paragraph:

-- Synthesis tool 53 analyzes the logic function and presents the IC designer with an implementation of that function using various types of standard cells that meet the specification. Different cell sizes will be used depending on the number of other cells their output is coupled to. The implementation typically contains a list of cells required and the necessary connections between them. The file that contains the list of connections is typically called a netlist 57. --